

Agent implanted via hardware, software, firmware
(such as ROM, Flash ROM, EPROM, microprocessor)

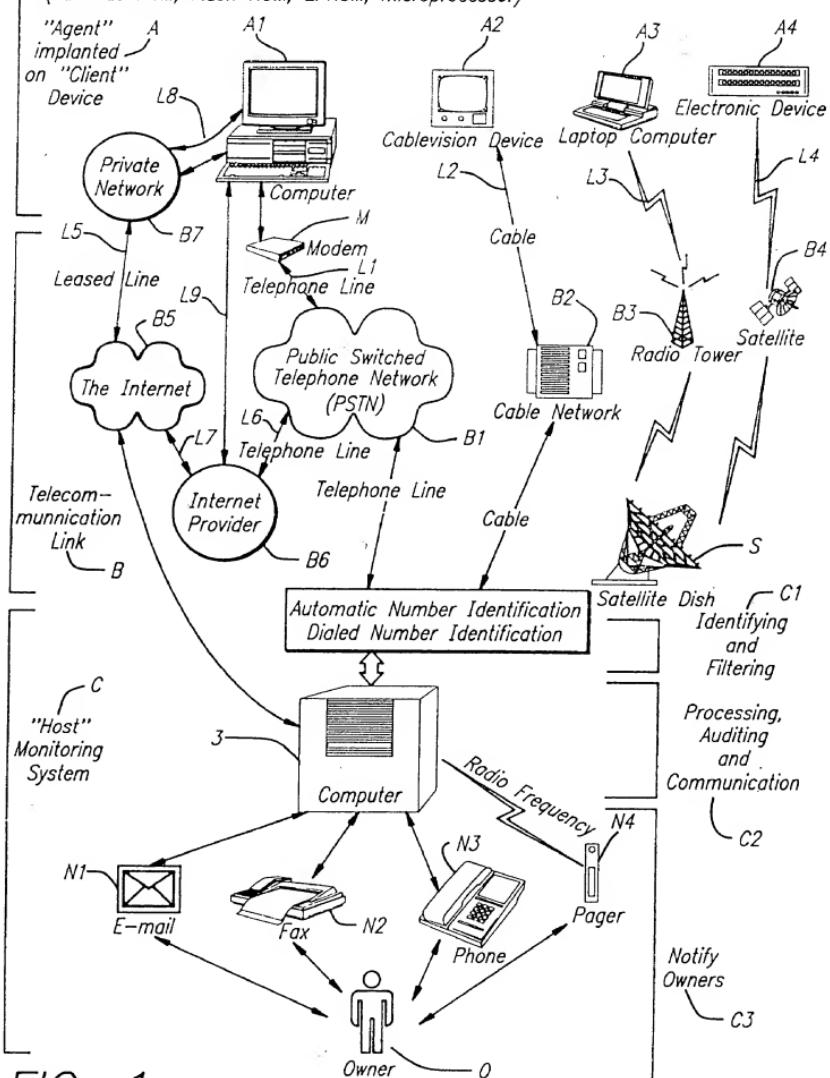
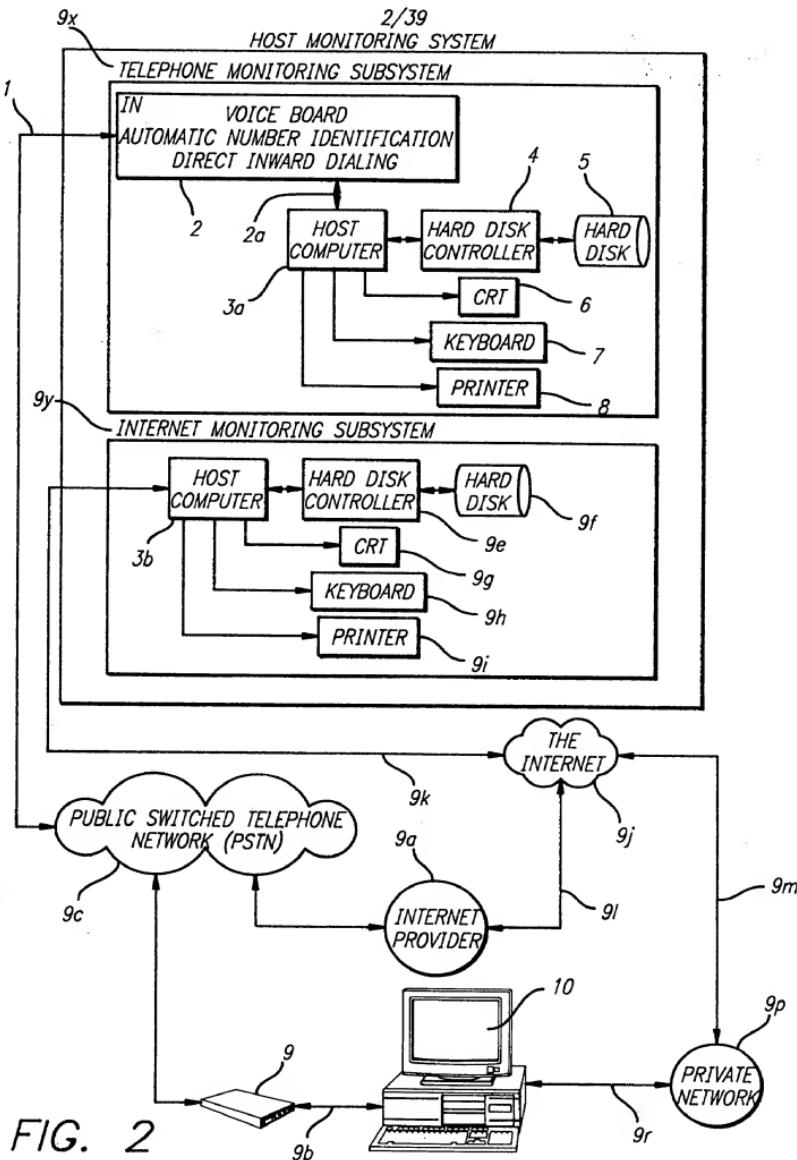


FIG. 1



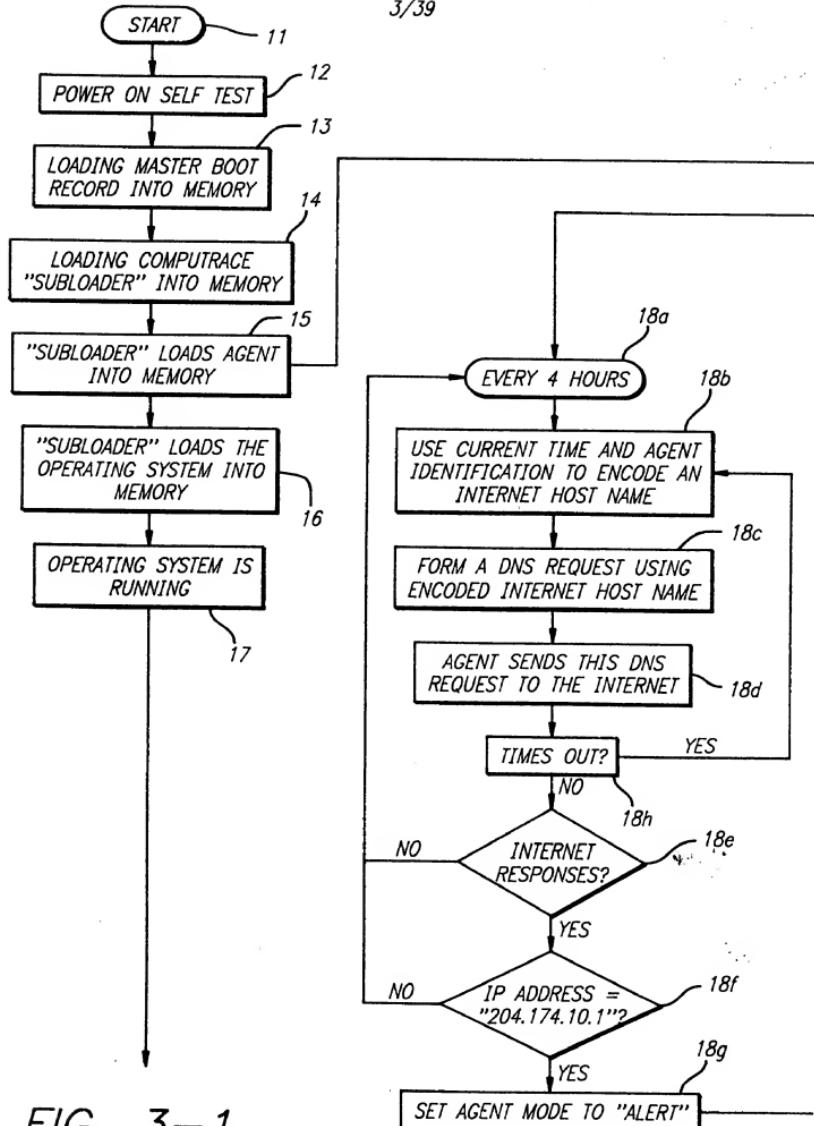


FIG. 3-1

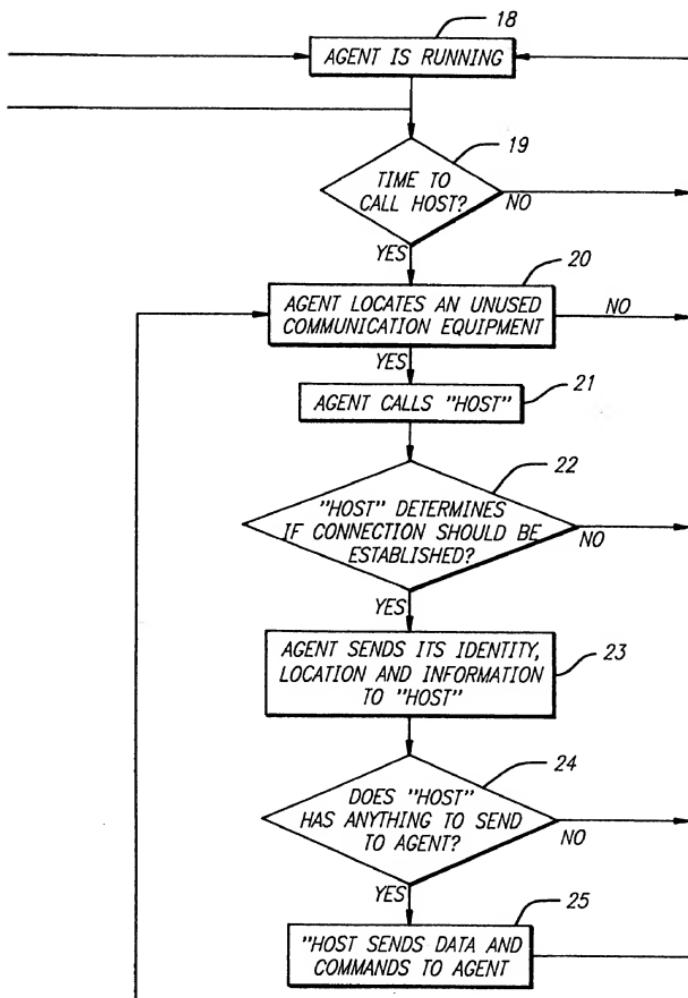


FIG. 3-2

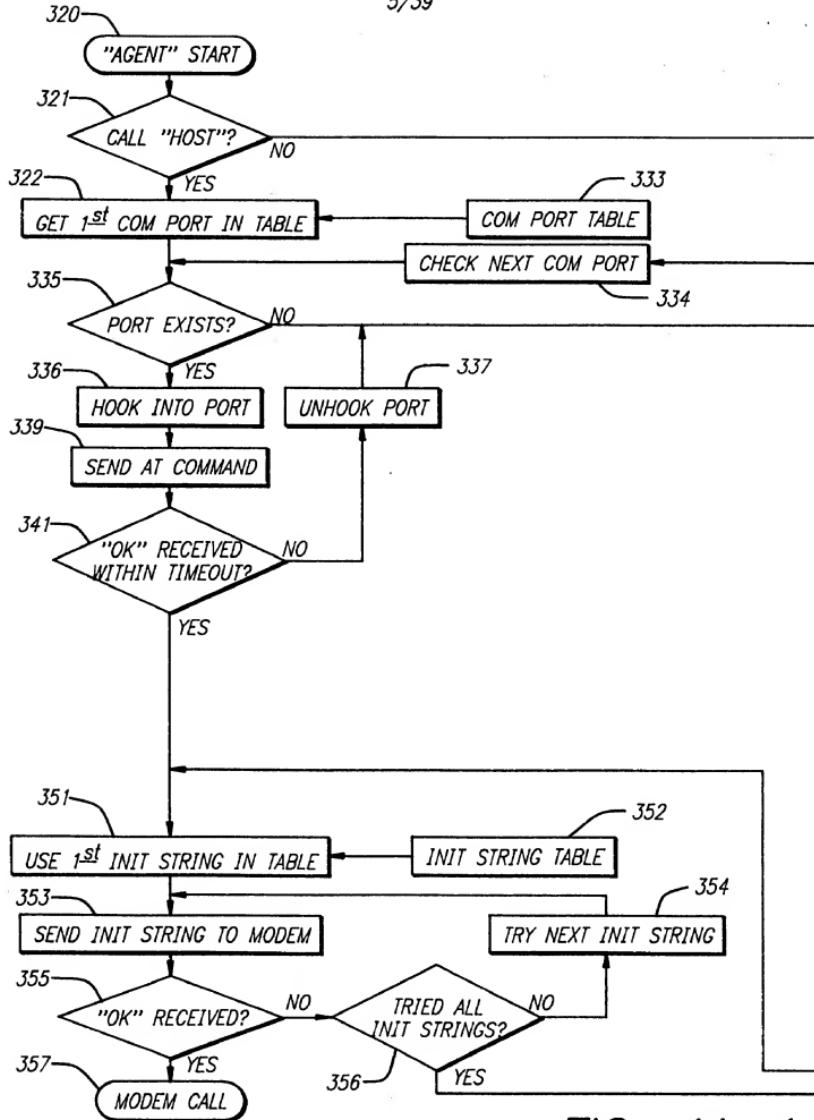
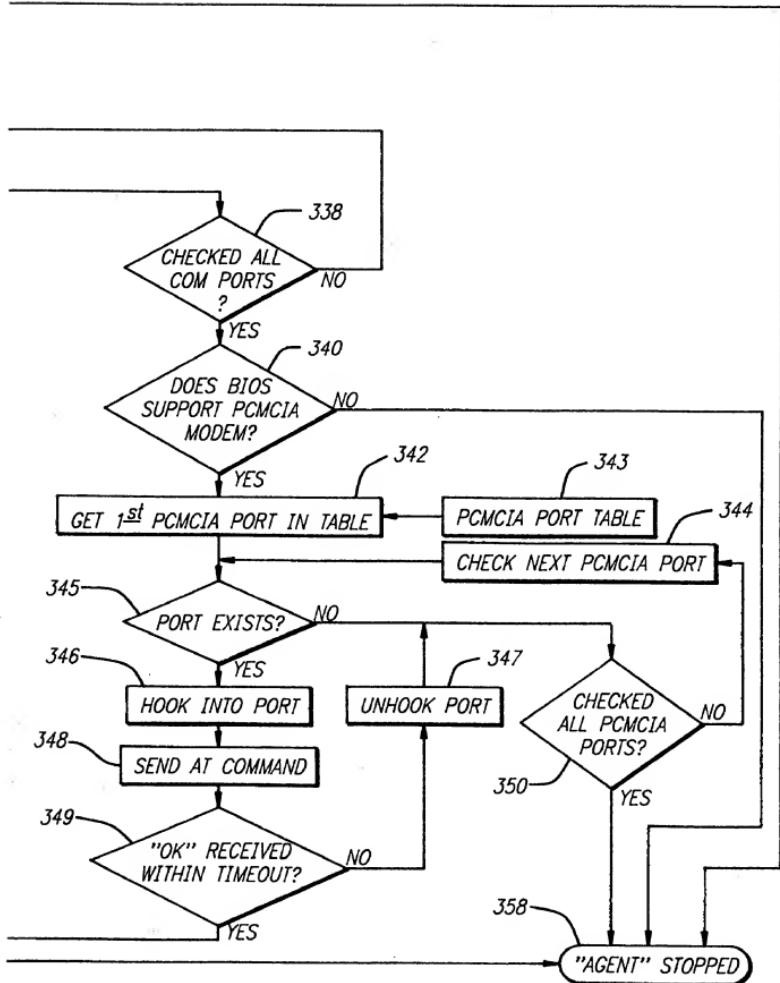


FIG. 4A-1

FIG. 4A-2



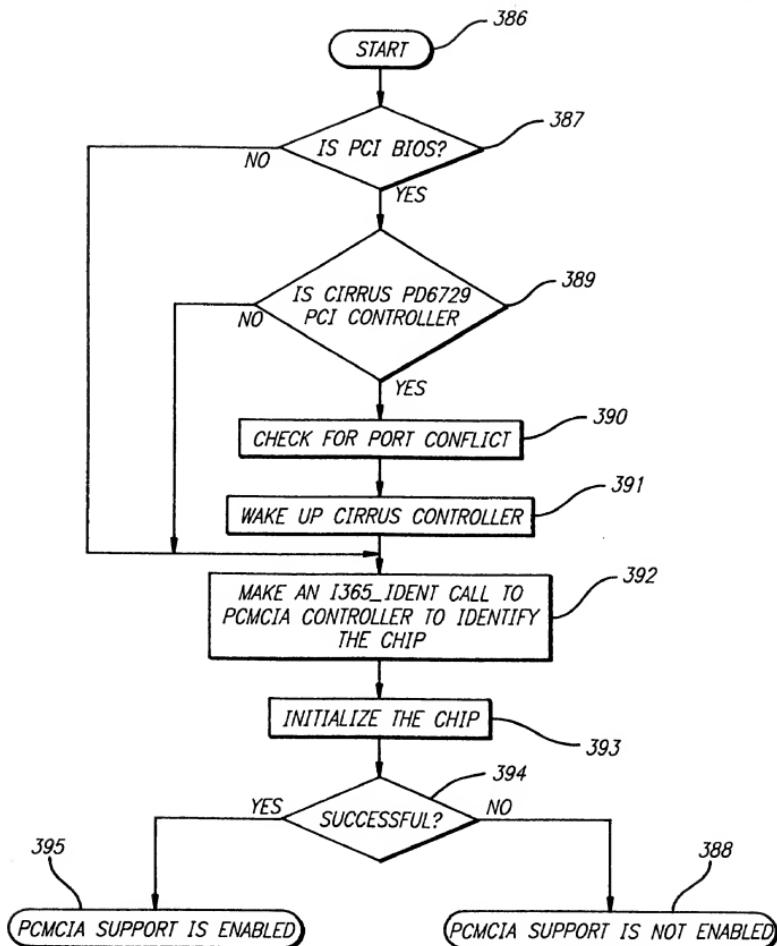


FIG. 4B

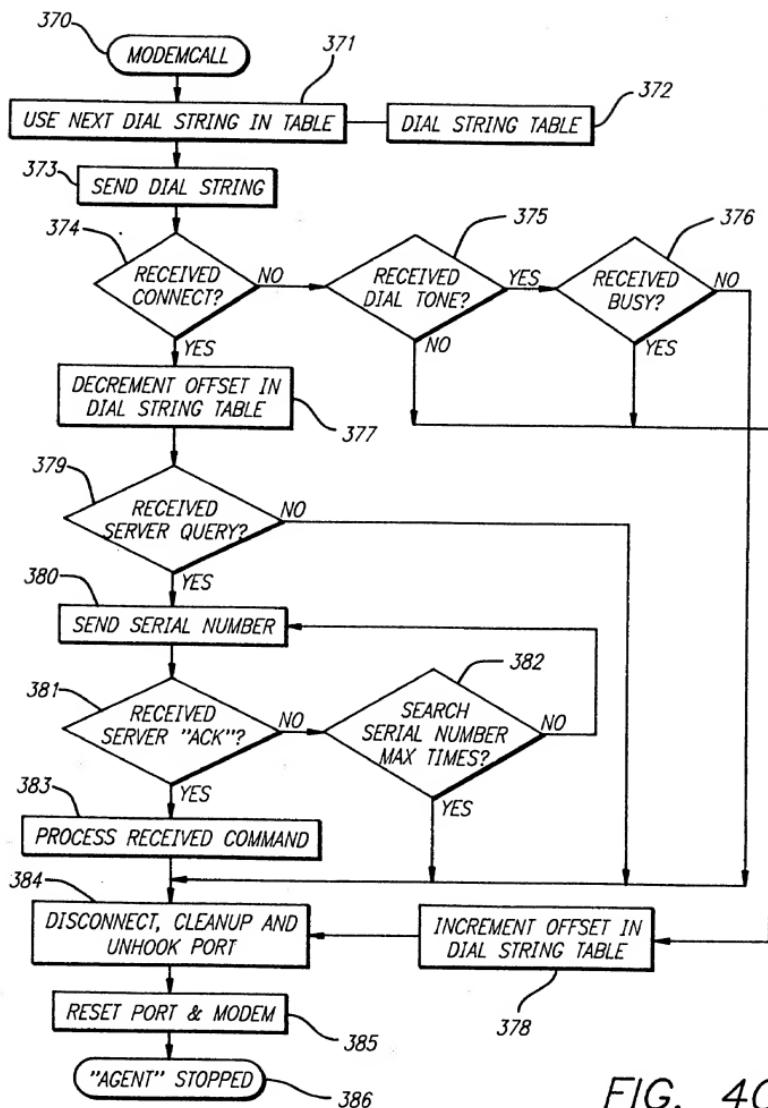


FIG. 4C

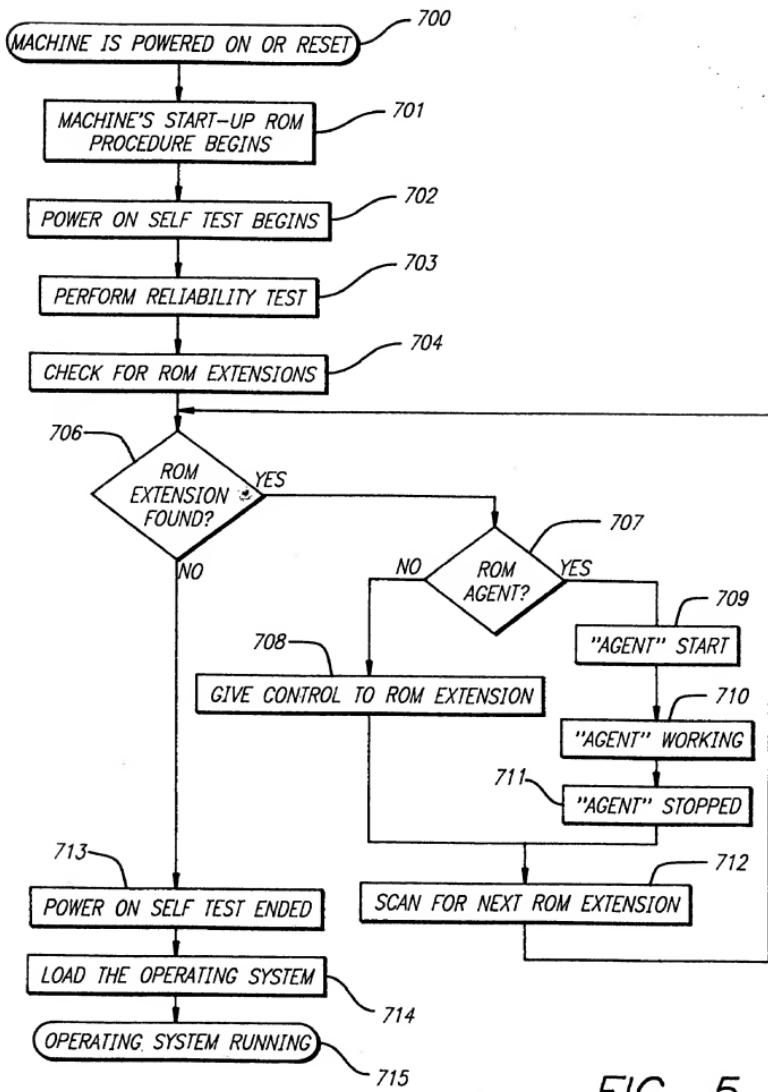


FIG. 5

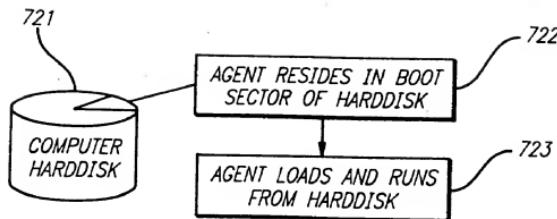


FIG. 6A

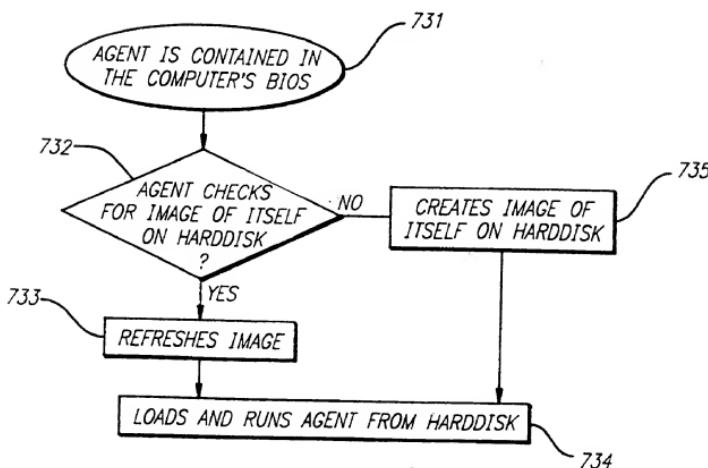


FIG. 6B

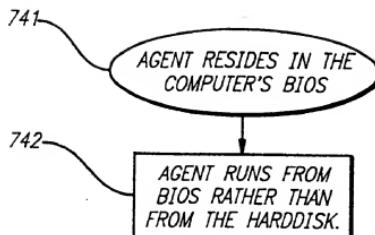


FIG. 6C

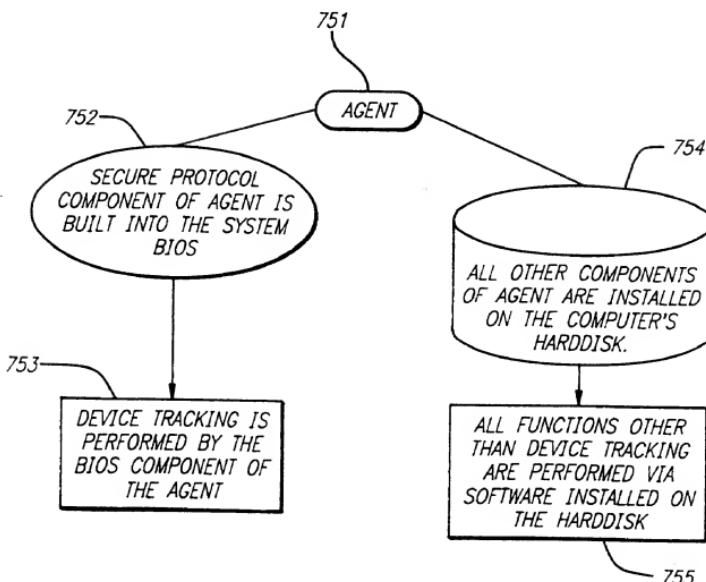


FIG. 7A

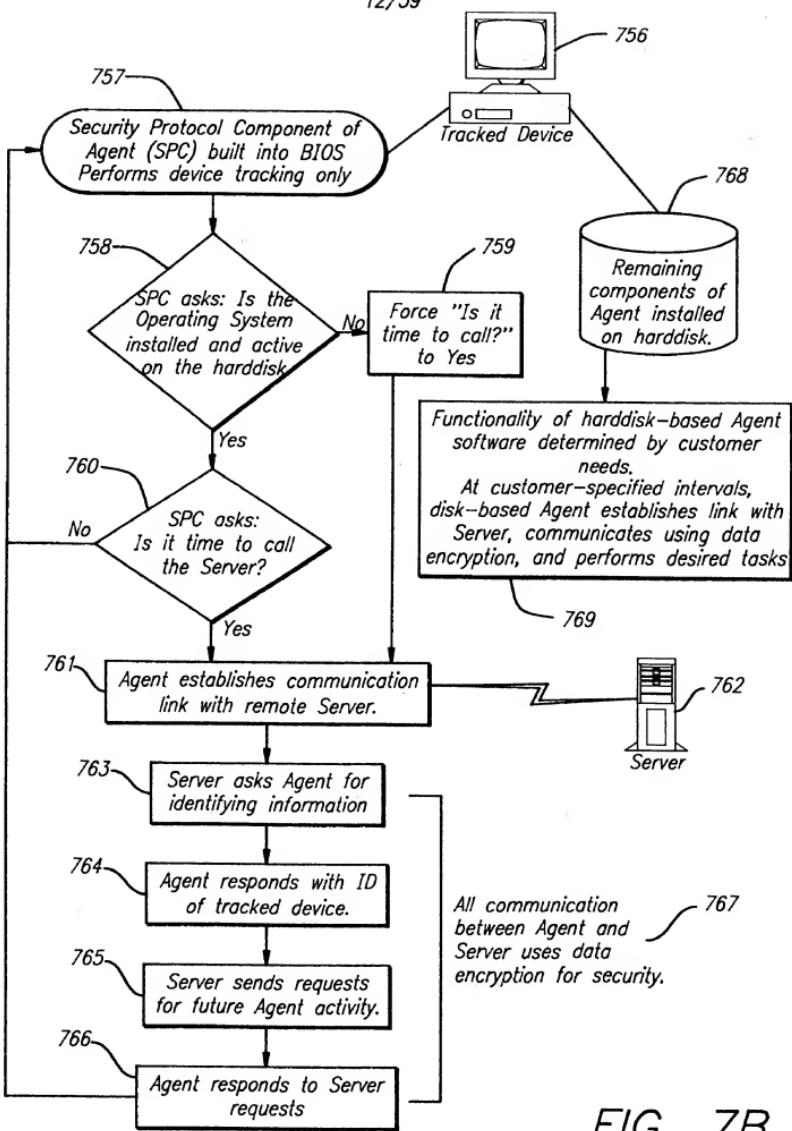


FIG. 7B

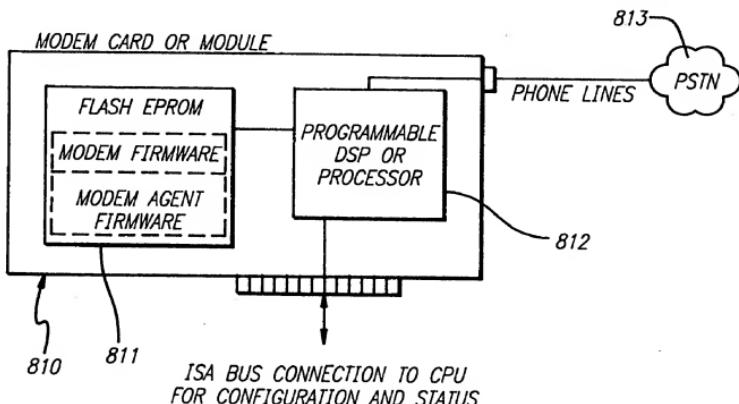
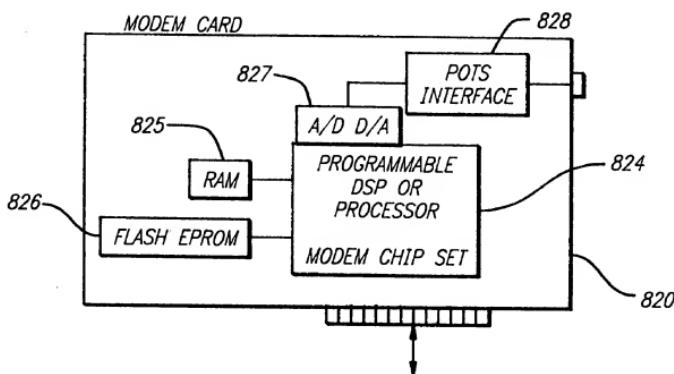


FIG. 8A



ISA BUS CONNECTION TO CPU
MAPPED TO I/O PORT ADDRESS

FIG. 8B

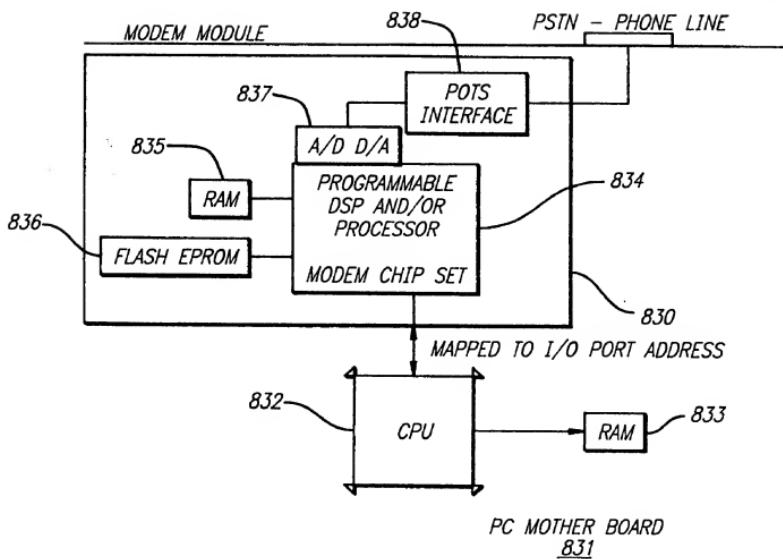


FIG. 8C

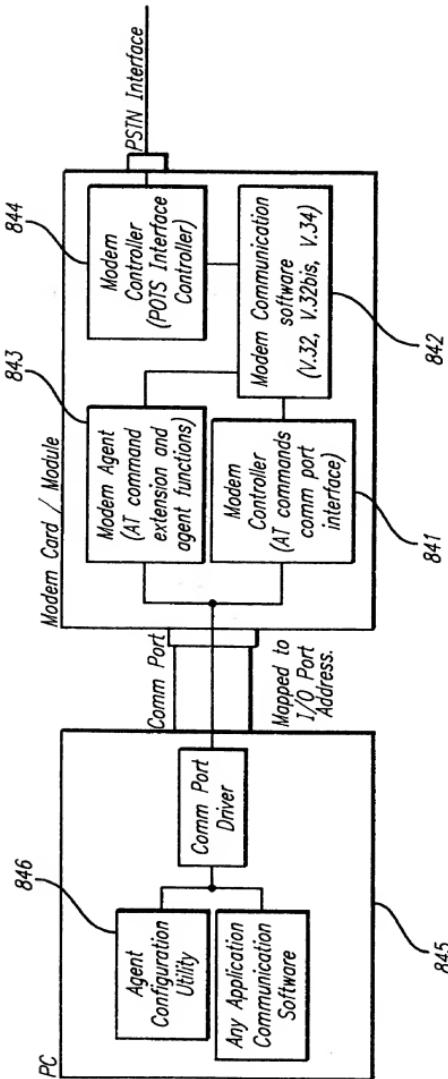


FIG. 8D

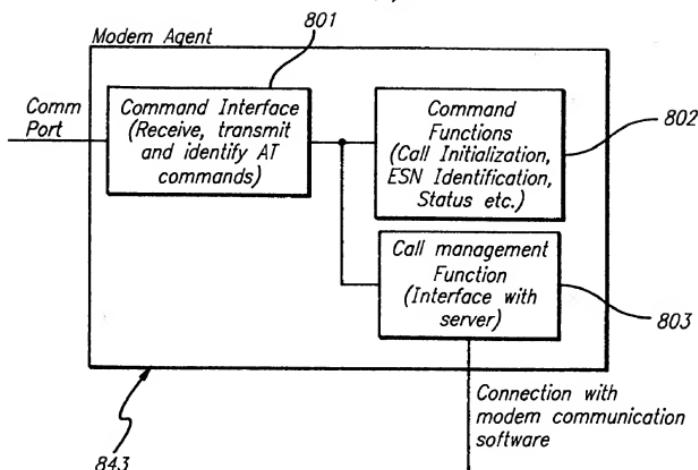


FIG. 8E

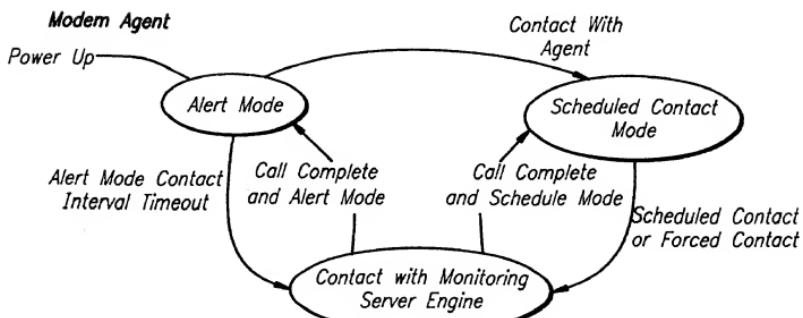


FIG. 8F

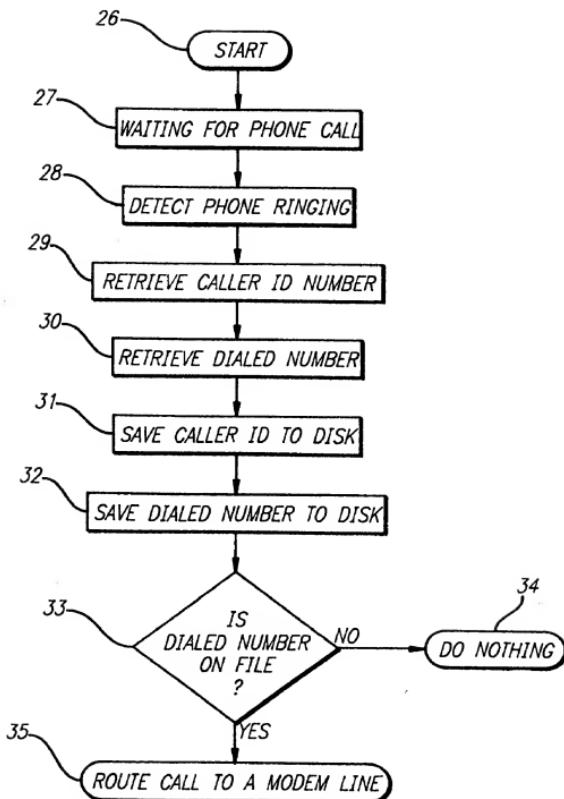


FIG. 9

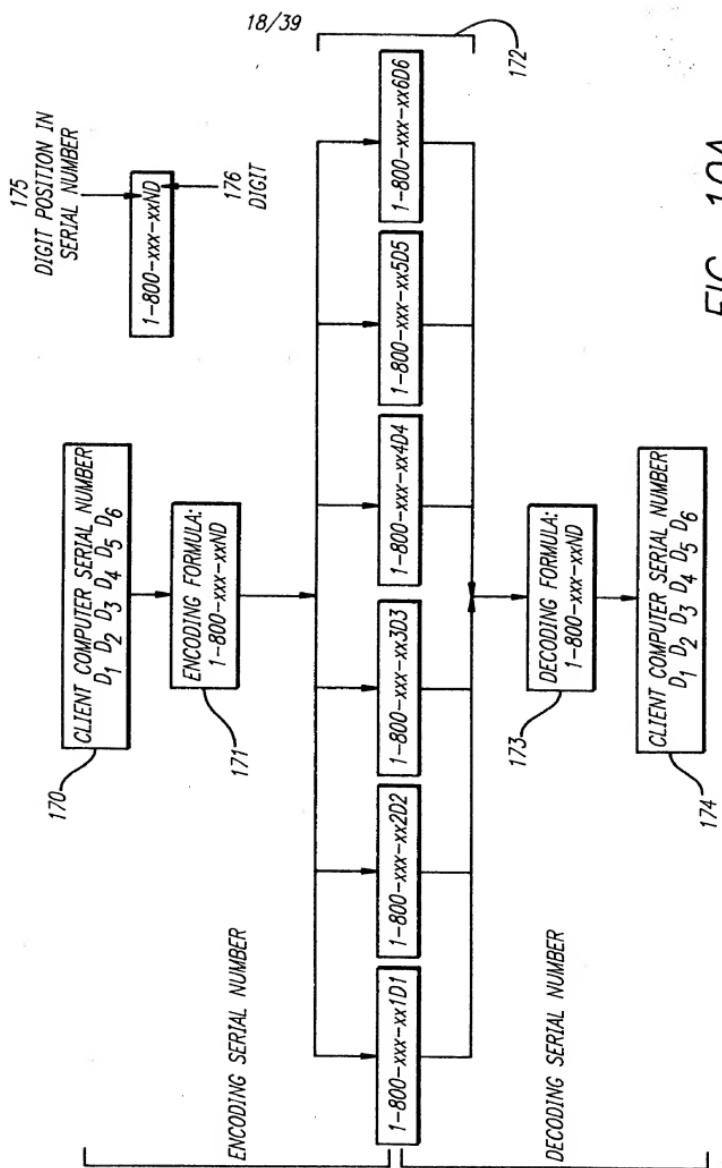
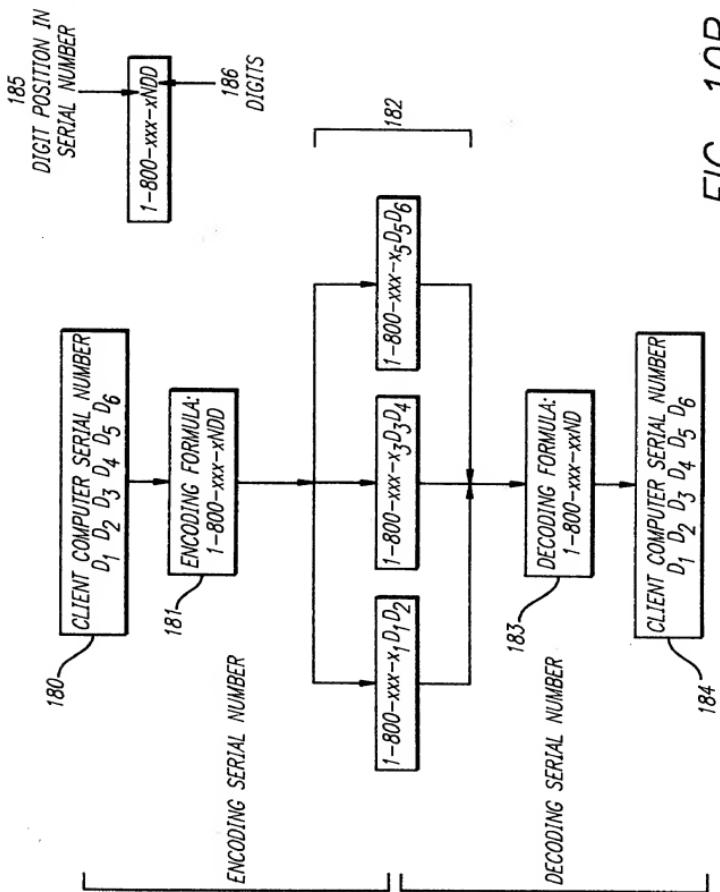


FIG. 10A



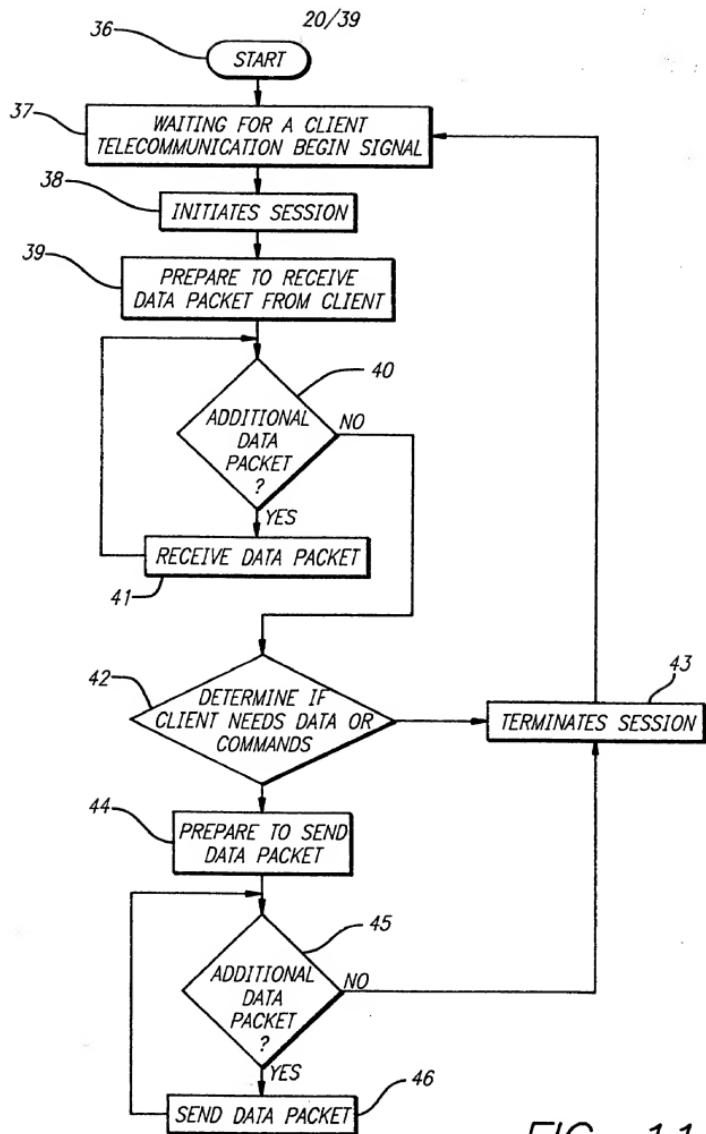


FIG. 11A

FIG. 11B

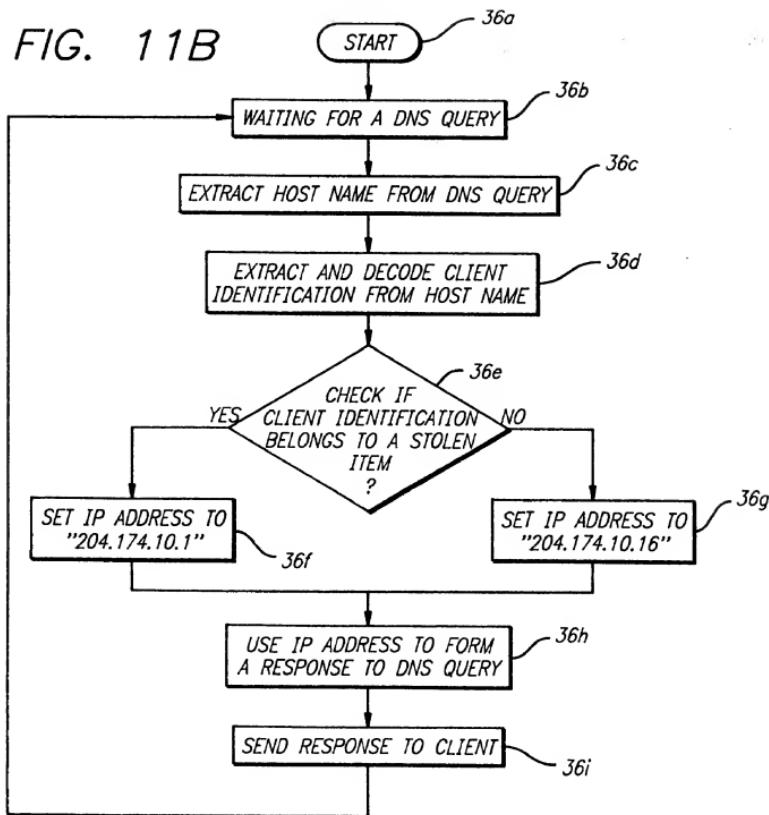


FIG. 11C

38
ENCODED CLIENT
IDENTIFICATION

YYMMDDHHMMSS.XXXXXXX.ipgrdx.as.com

DATE AND TIME 37

DOMAIN NAME 39

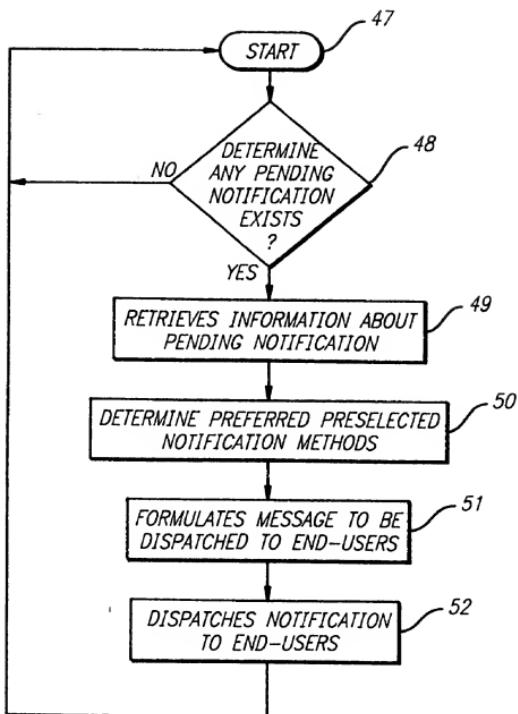
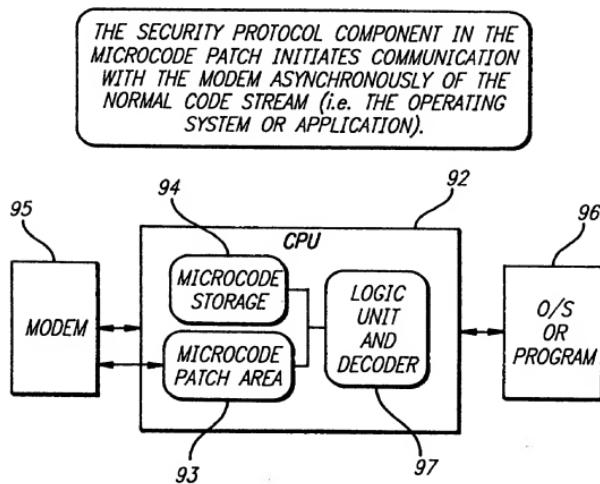
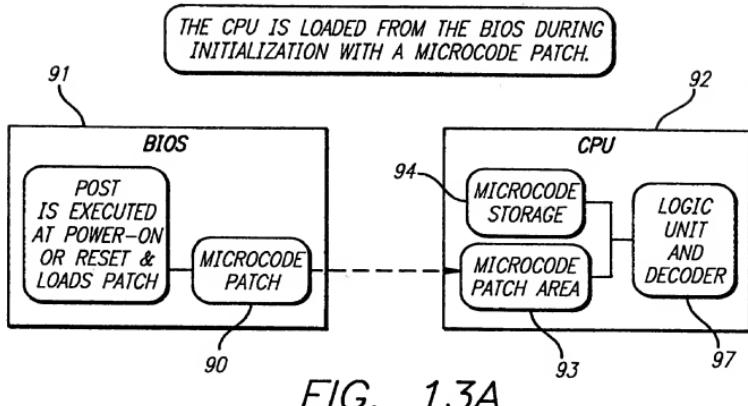


FIG. 12

**FIG. 13B**

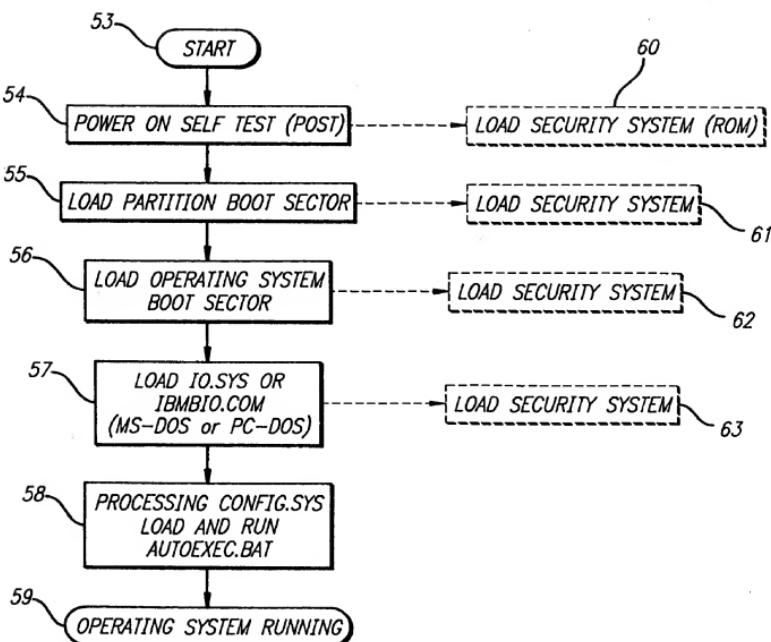


FIG. 14

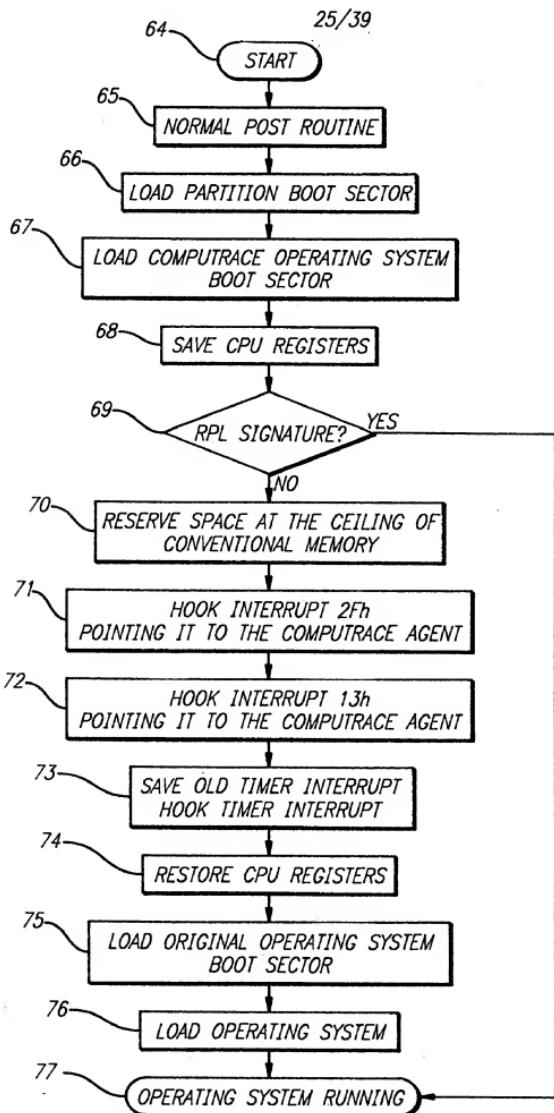


FIG. 14A

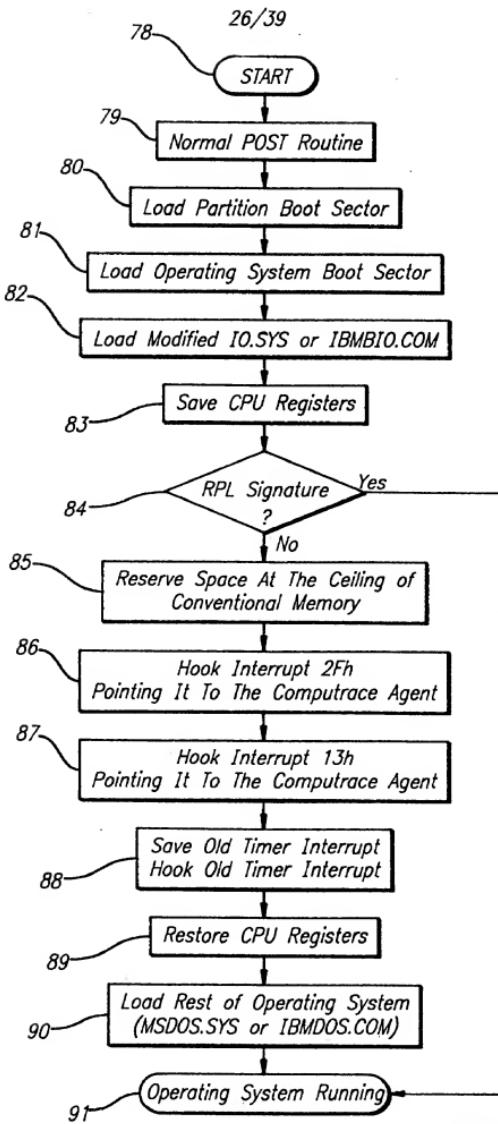


FIG. 14B

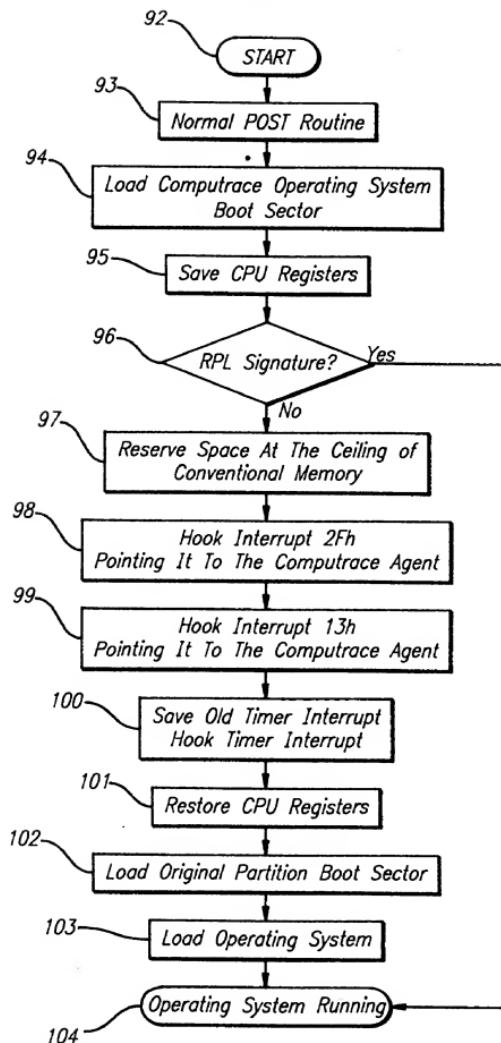


FIG. 14C

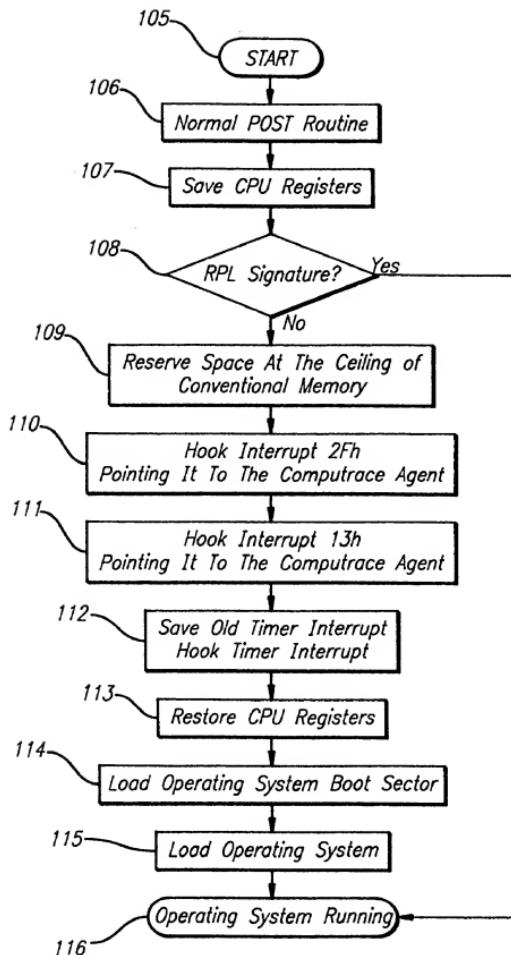


FIG. 14D

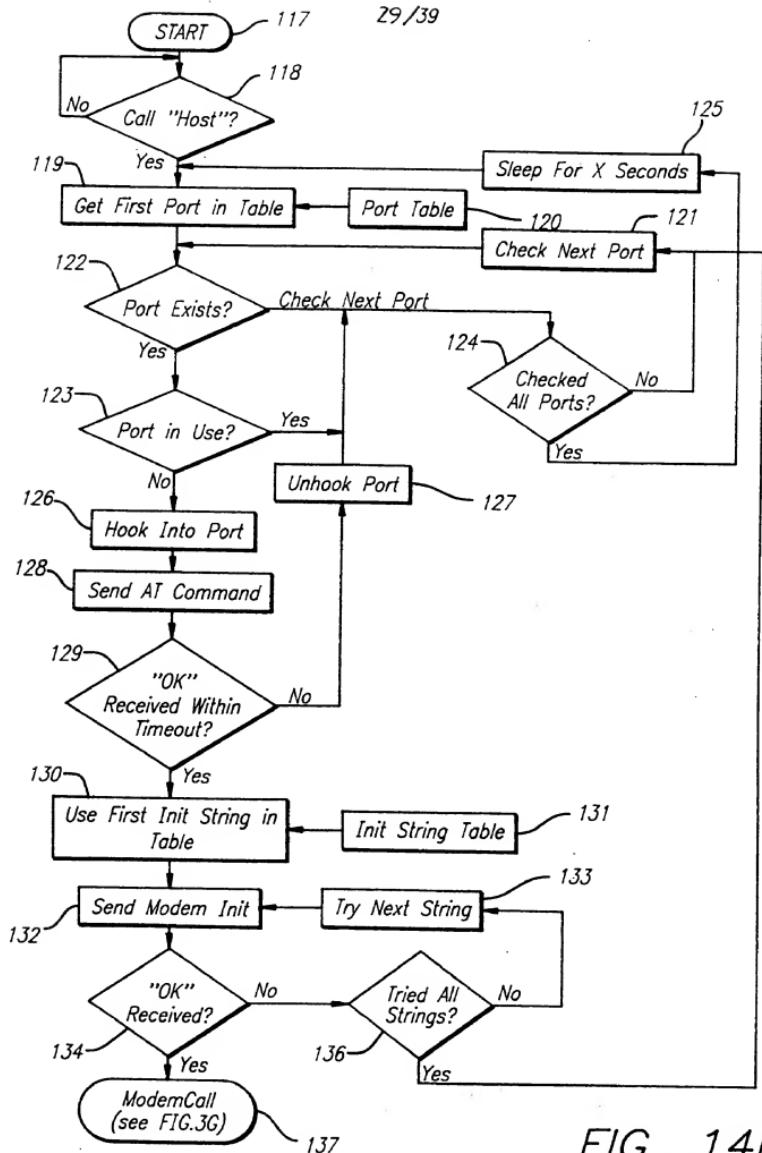


FIG. 14E

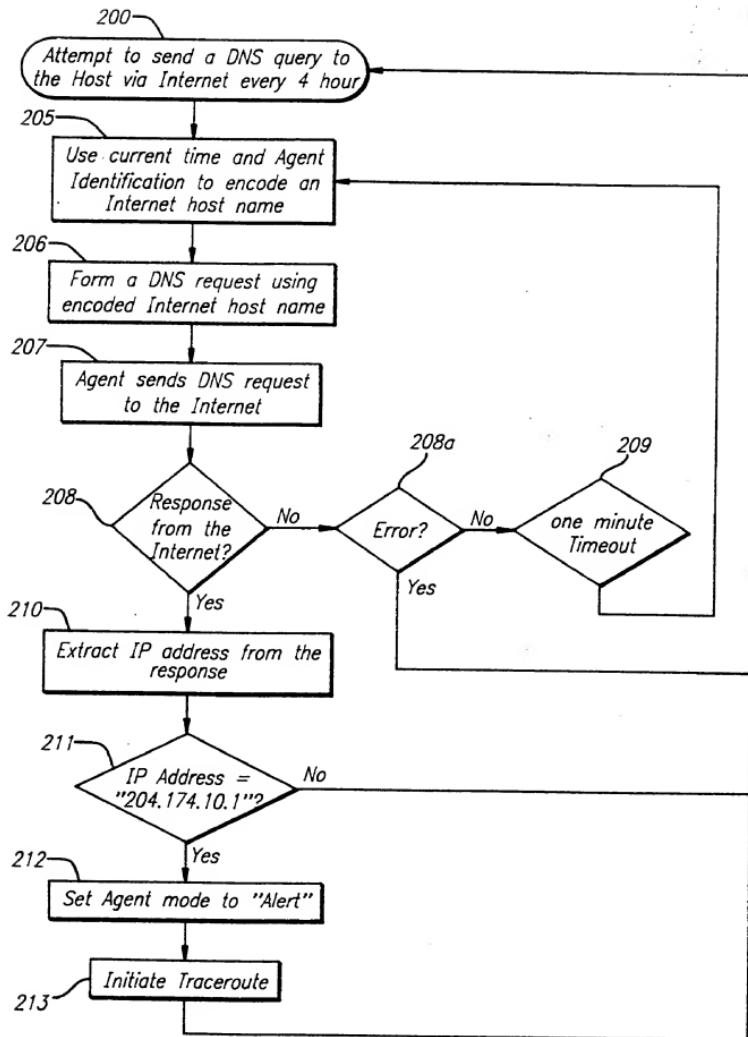


FIG. 14F

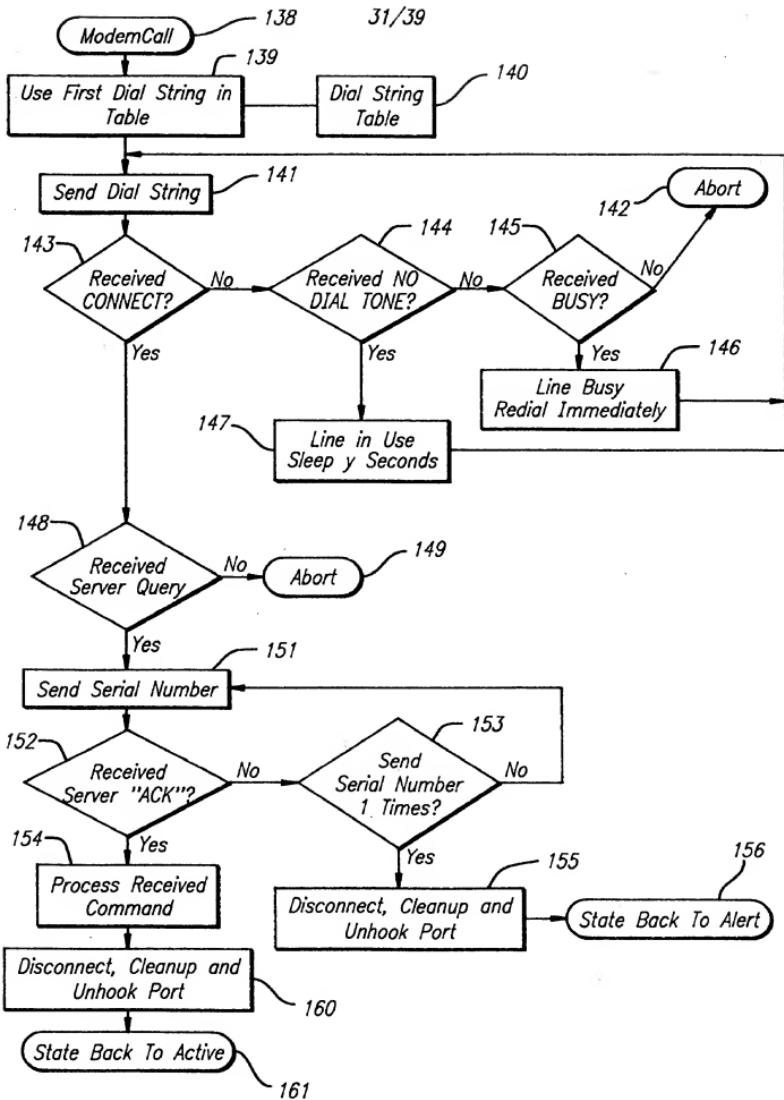


FIG. 14G

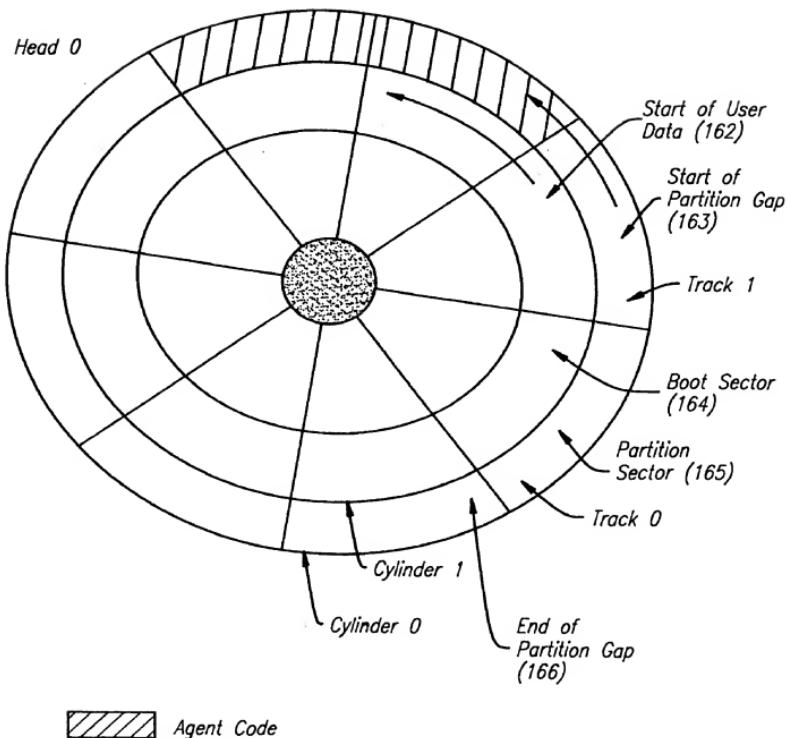
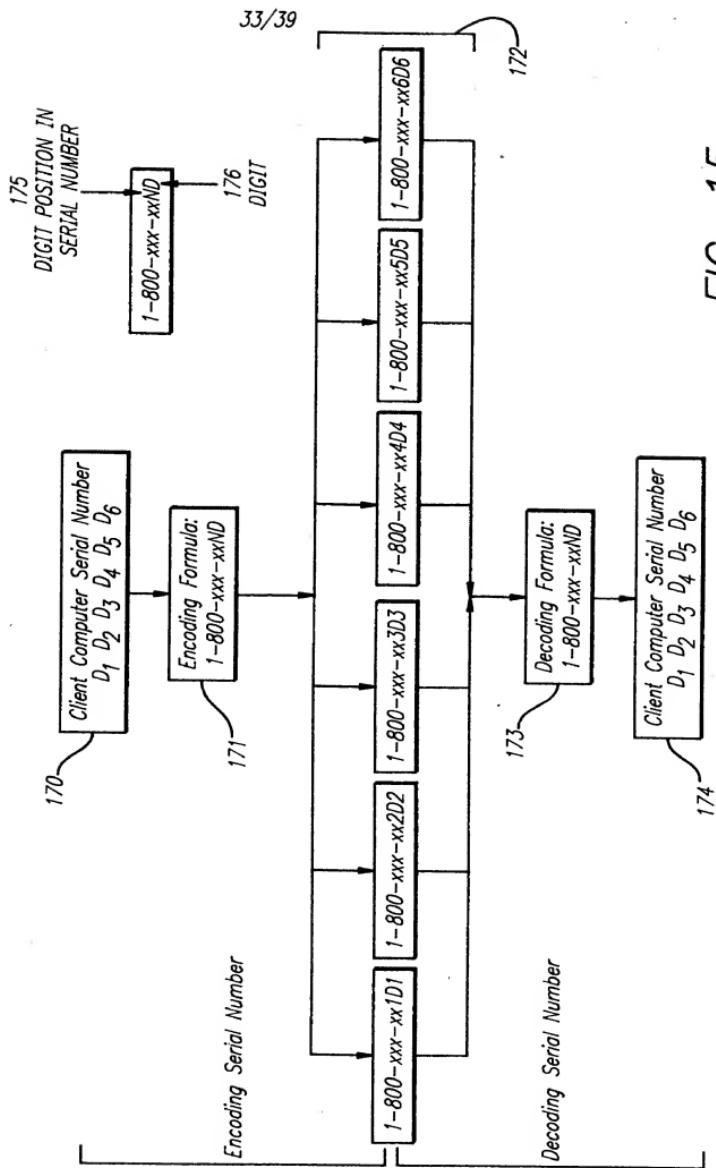
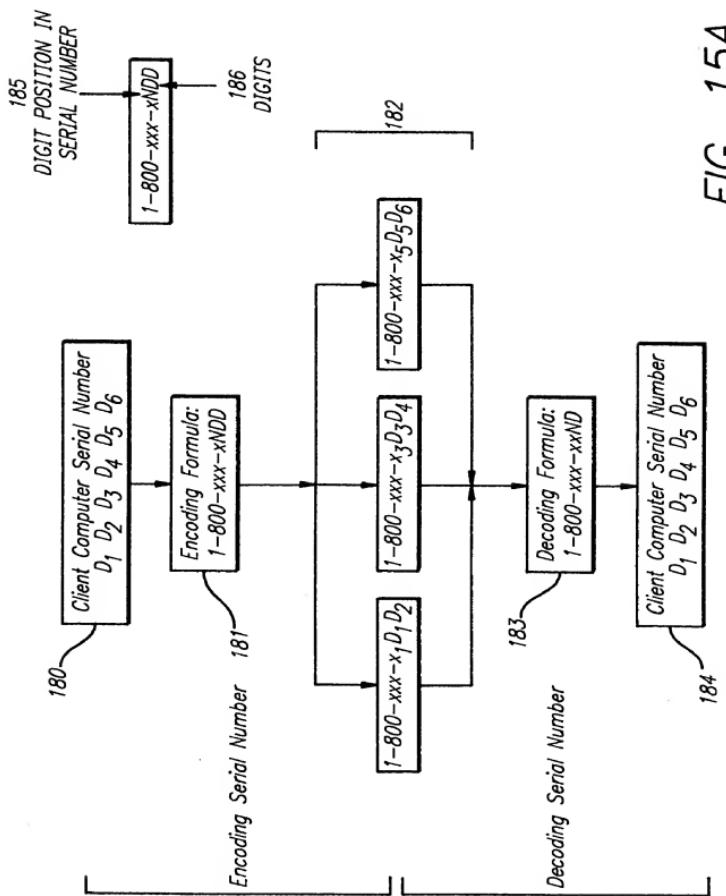


FIG. 14H





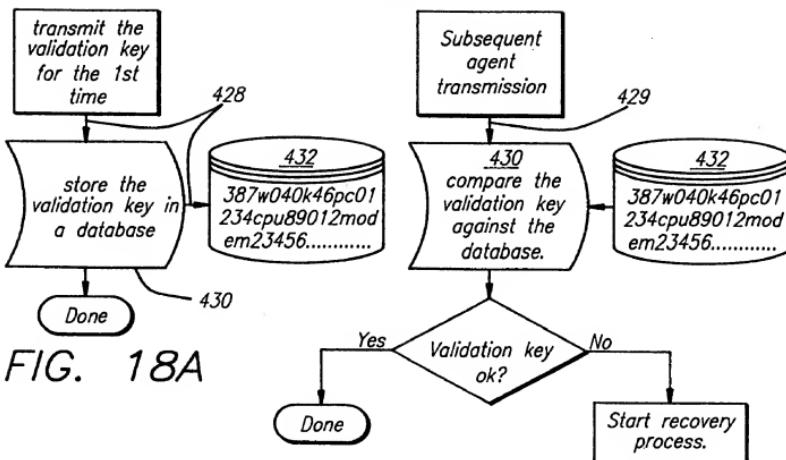
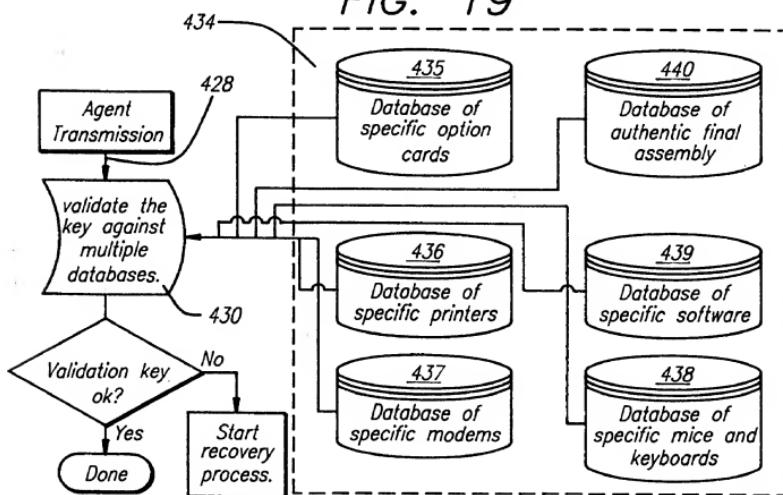


FIG. 19



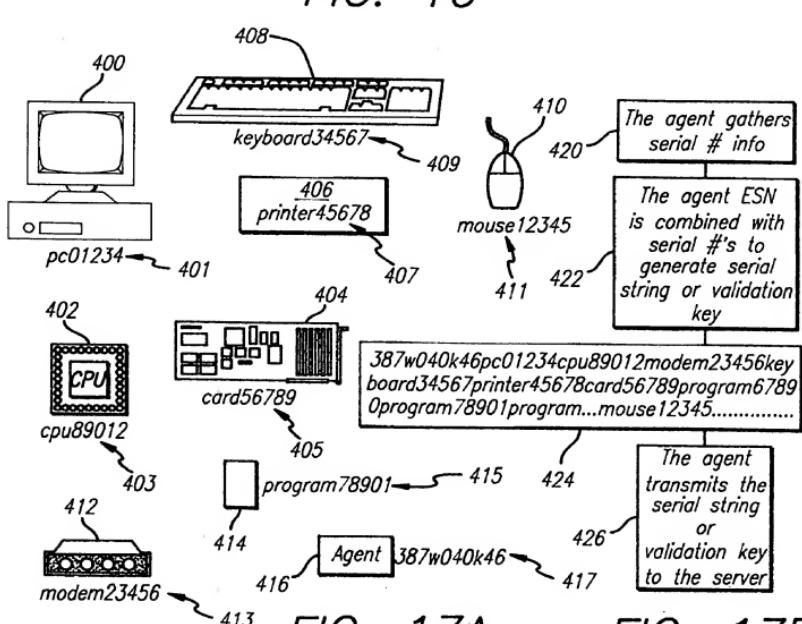
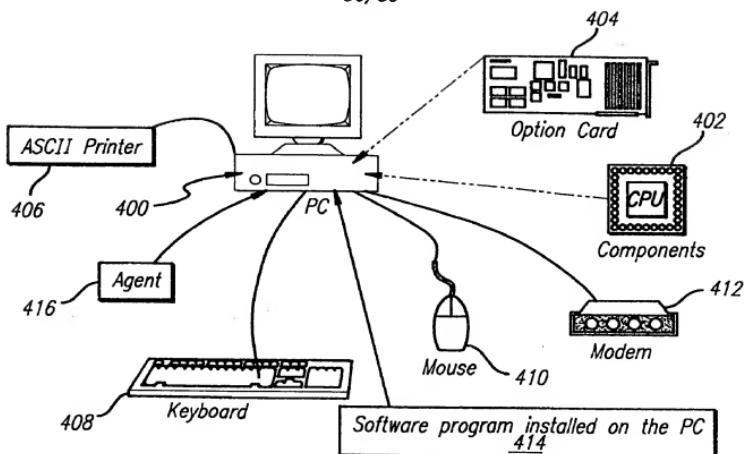
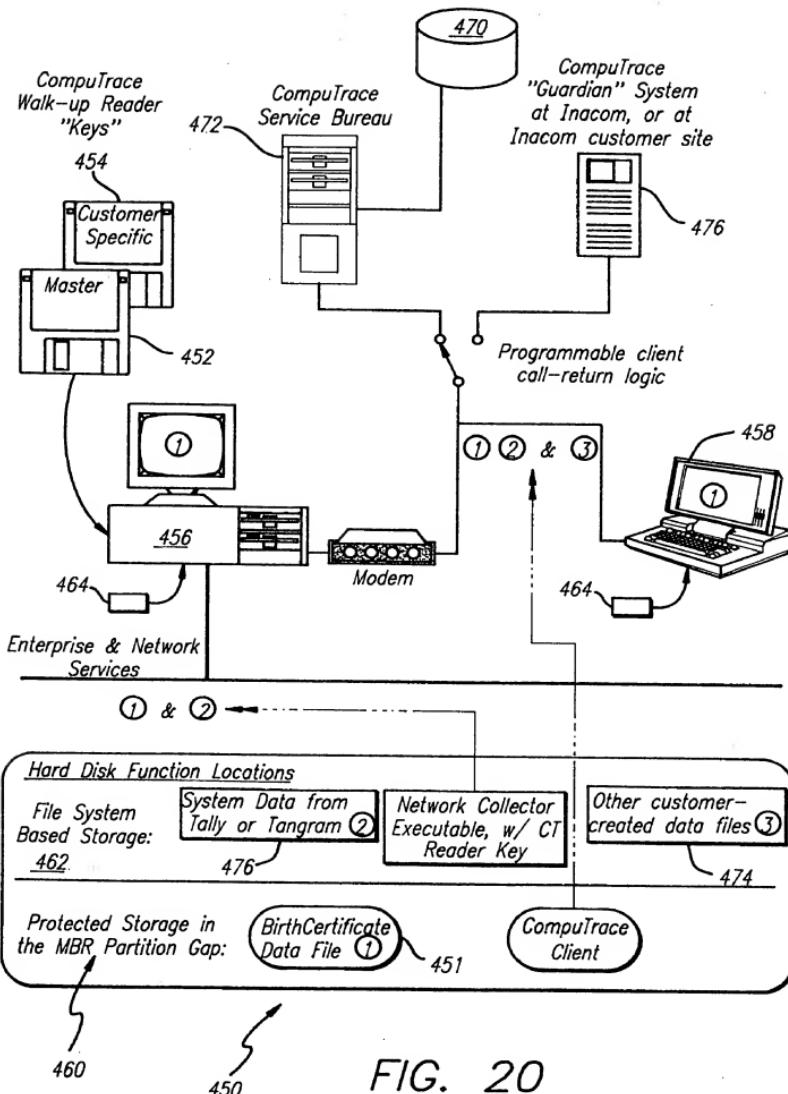


FIG. 17A

FIG. 17B



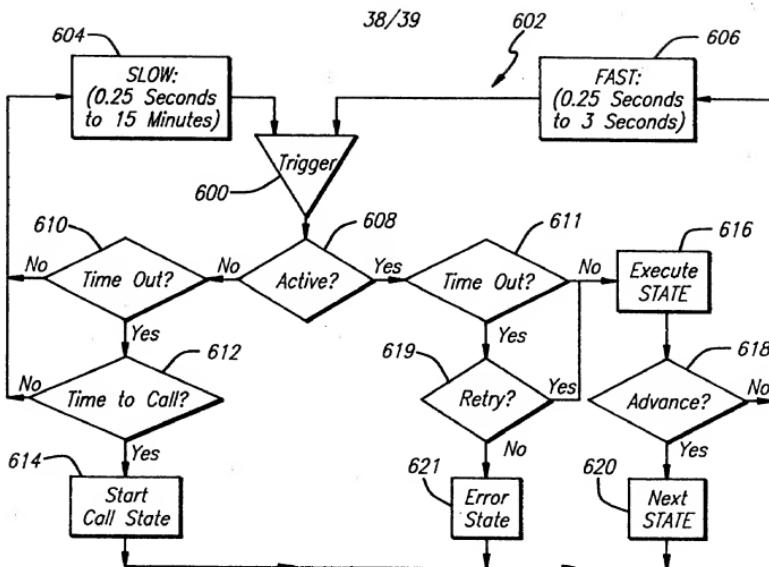


FIG. 21

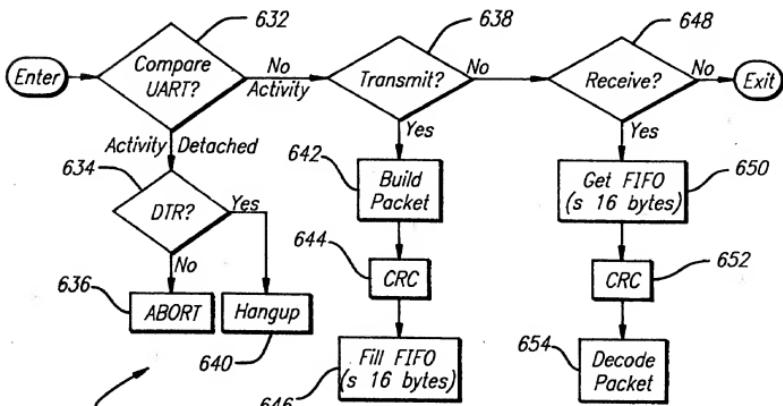


FIG. 22

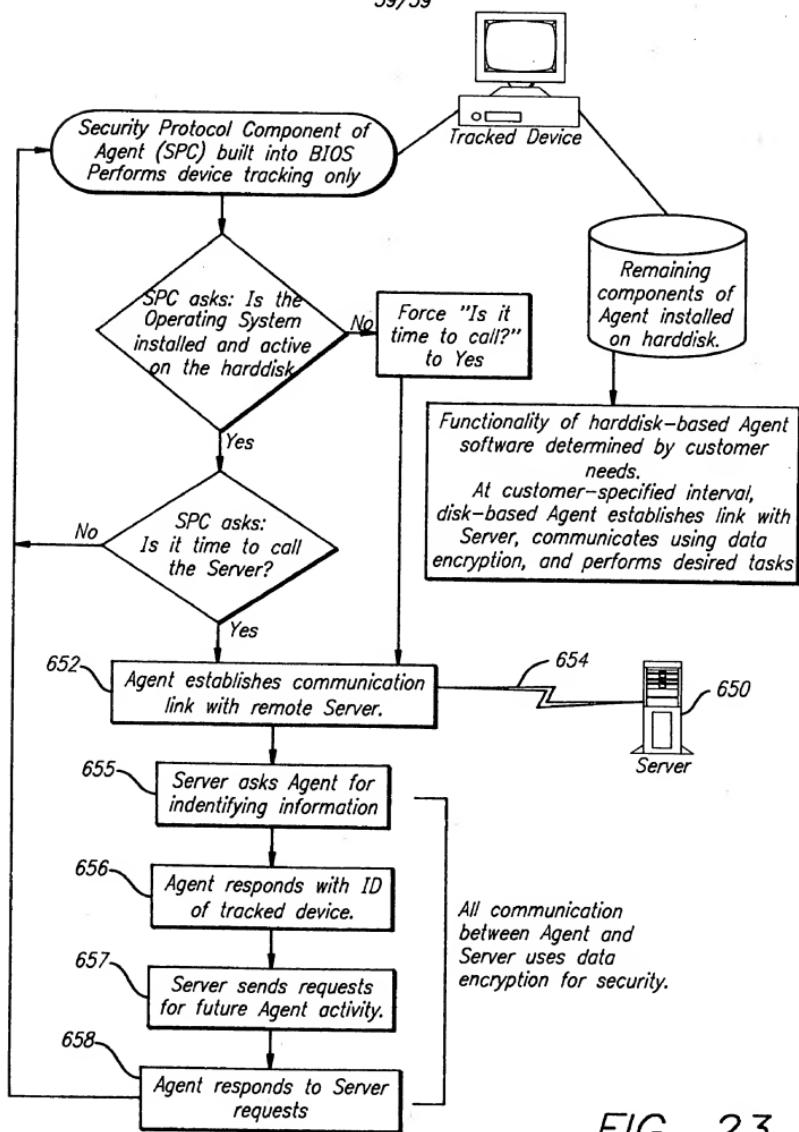


FIG. 23